

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 10-011301

(43)Date of publication of application : 16.01.1998

(51)Int.Cl.

G06F 9/46

(21)Application number : 08-164867

(71)Applicant : IMAI MASA HARU  
YAZAKI CORP

(22)Date of filing : 25.06.1996

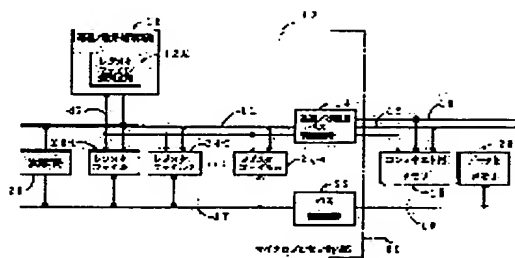
(72)Inventor : IMAI MASA HARU  
SHIOMI AKICHIKA  
NAKANO TAKUMI  
ITABASHI MITSUYOSHI  
KIROKU MASASHI  
YAMASE TAKAFUMI

## (54) MULTITASK PROCESSOR AND MULTITASK PROCESSING CONTROL METHOD

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To prevent the total performance of a microprocessor from decreasing even when the number of tasks to be processed is less than the number of register groups of the microprocessor.

**SOLUTION:** When it is necessary to save and reload a context, a saving/ reloading control means 12 saves a context stored in one of register groups 24-1 to 24-M in a context storage means 18 through saving/reloading buses 11, 15, and 16 provided independently of a bus 17 and reloads a context to be processed which is stored in the context storage means 18 to the register group, so ordinary task processing can be performed simultaneously with the saving and loading of the contexts to reduce the overhead of the saving/ reloading processing, thereby improving the total performance of the multitask processor.



### LEGAL STATUS

[Date of request for examination] 23.08.1999

[Date of sending the examiner's decision of rejection] 16.07.2002

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]